ZETTLER DISPLAYS

SPECIFICATIONS FOR LIQUID CRYSTAL DISPLAY

	CUSTOMER APP	ROVAL	
※ PART NO.	: ATM0800D9A (ZET	FLER DISPLAY	S) SPEC VER1.1
APPROVAL		COMPANY CHOP	
CUSTOMER COMMENTS			
COMMENTS			

ZETTLER DISPLAYS ENGINEERING APPROVAL								
DESIGNED BY CHECKED BY APPROVED BY								
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REVISION RECORD

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1. GENERAL SPECIFICATIONS

ltem	Specification	Remark
1. LCD size	8.0 inch(Diagonal)	
2. Driver element	a-Si TFT active matrix	
3. Resolution	800x(RGB)x600	
4. Display mode	Normally white, Transmissive	
5. Dot Pitch (W*H)	0.0675(W) × 0.2025(H) mm	
6. Pixel pitch(W*H)	0.2025 (W) × 0.2025(H) mm	
7. Active Area(W*H)	162.0(W) × 121.5(H) mm	
8. Module size (W*H)	183.0(W) × 141.0(H) × 6.4(D) mm	Note 1
9. Surface treatment	Anti-glare	
10. Color arrangement	RGB-stripe	
11. Color	16.7M	
12. Viewing angle (L/R/T/B)	70/70/70/50	
13. Interface	24bit RGB interface	
14. LCD controller	N.A	
15. LCM brightness	350 cd/m2 (Typ.)	
16. Backlight driving condition	200mA @ 9.2V(Typ.)	
17. Touch panel	N.A	
18. Touch controller	N.A	
19. Operation temperature	-20~70 °C	
20. Weight	TBD	
21. RoHS	RoHS compliant	

Note 1: Please refer to mechanical drawing.

2. PIN ASSIGNMENT

TFT LCD Panel Driving Section

FPC Connector is used for the module electronics interface.

The recommended model is FH12A-50S-0.5SH manufactured by Hirose.

Pin No.	Symbol	Function	Level	Note
1~4	NC	No connection		
5	GND	Power ground	Р	
6	VCOM	Common voltage	I	
7	VCC	Power supply	Р	
8	MODE	DE/SYNC mode select	I	Note3
9	DE	Data enable signal for RGB interface operation	I	
10	VS	Vertical sync	I	
11	HS	Horizontal sync	I	
12~19	B7~B0	Blue data (B7: MSB / B0: LSB)	I	
20~27	G0~G7	Green data (G7: MSB / G0: LSB)	I	
28~35	R7~R0	Red data (R7: MSB / R0: LSB)	I	
36	GND	Power ground	Р	
37	DCLK	Sample clock	I	
38	GND	Power ground	Р	
39	L/R	Right/ left selection	I	Note2,5
40	U/D	Up/down selection	I	Note2,5
41	VGH	Gate ON voltage	Р	
42	VGL	Gate OFF voltage	Р	
43	AVDD	Power for Analog circuit	Р	
44	RESET	Global reset pin.	I	Note1
45	NC	No connection		
46	VCOM	Common voltage	I	
47	DITHB	Dithering function	I	Note 4
48	GND	Power ground	Р	
49~50	NC	No connection		

I: input, O: output, P: Power

Note 1: Global reset pin. Active Low to enter Reset State. Suggest to connecting with an RC reset circuit for stability. Normally pull high.

Setting of sca	an control input	Coorning direction			
U/D	R/L	Scanning direction			
GND	VCC	Up to down, left to right			
VCC	GND	Down to up, right to left			
GND	GND	Up to down, right to left			
VCC	VCC	Down to up, left to right			

Note 3: DE/SYNC mode select, Normally pull high.

H: DE mode.

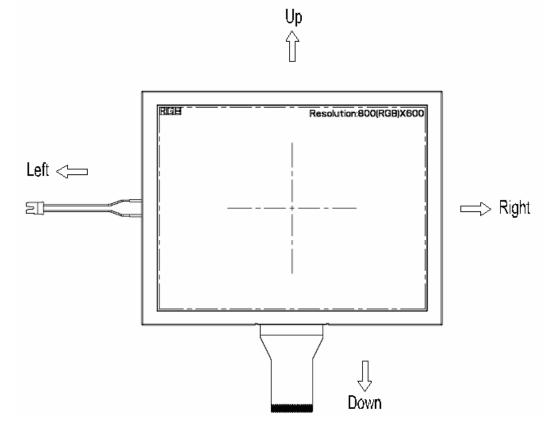
L: HS/VS mode.

Note4: Dithering function enable control.Normally pull high.

DITHB="1", Disable internal dithering function. For 18bit RGB interface, connect two LSB bits of all the R/G/B data buses to GND.

DITHB="0", Enable internal dithering function, For TTL 24bit parallel RGB image data input.

Note 5: Definition of scanning direction.Refer to the figure as below:



3. Operating Specification

3.1 ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Val	ues	Unit	Remark
item	Symbol	Min.	Max.	Onit	Reinark
		-0.3	5.0	V	
	AV _{DD}	-0.5	13.5	V	
Power Voltage	V _{GH}	13.0	19.0	V	
	V _{GL}	-12.0	-2.0	V	
	V_{GH} - V_{GL}		31	V	
Operation Temperature	T _{OP}	-20	70	°C	
Storage Temperature	T _{ST}	-30	80	°C	
LED Reverse Voltage	V _R	-	1.2	1.2 V Each	
LED Forward Current	١ _F		25	mA	Each LED

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 2: V_R Conditions: Zener Diode 20mA

3.1.1 Typical Operation Conditions

ltem	Symbol		Unit	Remark		
item	Symbol	Min.	Тур.	Max.	Onic	Kennark
		3.1	3.3	3.5	V	Note 2
Power Voltage	AV _{DD}	10.2	10.4	10.6	V	
	V _{GH}	15.3	16.0	16.7	V	
	V _{GL}	-7.7	-7	-6.3	V	
Input Signal Voltage	V _{COM}	2.8	3.8	4.8	V	
Input Logic High Voltage	V _{IH}	0.8DV _{DD}			V	Note 3
Input Logic Low Voltage	V _{IL}	0		$0.2 DV_{DD}$	V	Note 3

Note 1: Be sure to apply DV_{DD} and V_{GL} to the LCD first, and then apply V_{GH} .

Note 2: DV_{DD} setting should match the signals output voltage (refer to Note 3) of customer's system board.

Note 3: DCLK,HS,VS,RESET,U/D, L/R,DE,R0~R7,G0~G7,B0~B7,MODE,DITHB.

3.1.2 Current Consumption

ltem	Symbol		Values	Unit	Remark		
item	Symbol	Min.	Тур.	Max.	Onit	Reillark	
Power Voltage	I _{GH}		0.2	0.5	mA	V _{GH} =16.0V	
	I _{GL}		0.2	1.0	mA	V _{GL} =-7.0V	
	I _{DVDD}		5.5	10	mA	DV _{DD} =3.3V	
	I _{AVDD}		32	50	mA	AV _{DD} =10.4V	

3.1.3 Backlight driving conditions

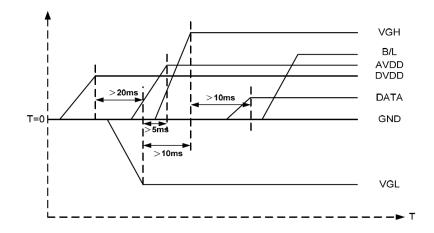
Item	Symbol		Unit	Remark		
nem	Symbol	Min.	Тур.	Max.	Onit	I CHIIMIN
Voltage for LED Backlight	VL	8.8	9.2	9.6	V	Note 1
Current for LED Backlight	۱ _L		200		mA	
LED life time		20000			Hr	Note 2

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25 $^{\circ}$ C and I_L =200mA.

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25℃ and I_L =200mA.

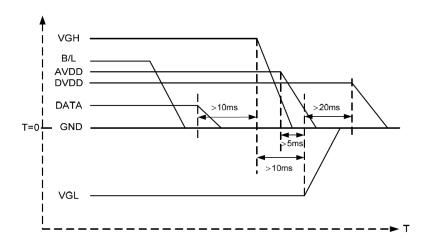
3.2 Power Sequence

a.Power on:









 $B/L{\rightarrow}Data{\rightarrow}V_{GH}{\rightarrow}V_{GL}{\rightarrow}DV_{DD}$

Note: Data include R0~R7, B0~B7, GO~G7, U/D, L/R, DCLK, HS,VS,DE.

3.3 Timing Characteristics

3.3.1 AC Electrical Characteristics

ltem	Symbol		Values		Unit	Remark
item	Symbol	Min.	Тур.	Max.	Onit	Remark
HS Setup Time	Thst	8			ns	
HS Hold Time	Thhd	8			ns	
VS Setup Time	Tvst	8			ns	
VS Hold Time	Tvhd	8			ns	
Data Setup Time	Tdsu	8			ns	
Data Hold Time	Tdhd	8			ns	
DE Setup Time	Tesu	8			ns	
DE Hold Time	Tehd	8			ns	
DV _{DD} Power On Slew Time	TPOR			20	ms	From 0 to 90% DV _{DD}
Reset Pulse Width	TRst	10			ms	
DCLK Cycle Time	Tcoh	20			ns	
DCLK Pulse Duty	Tcwh	40	50	60	%	

3.3.2 Data Input Format

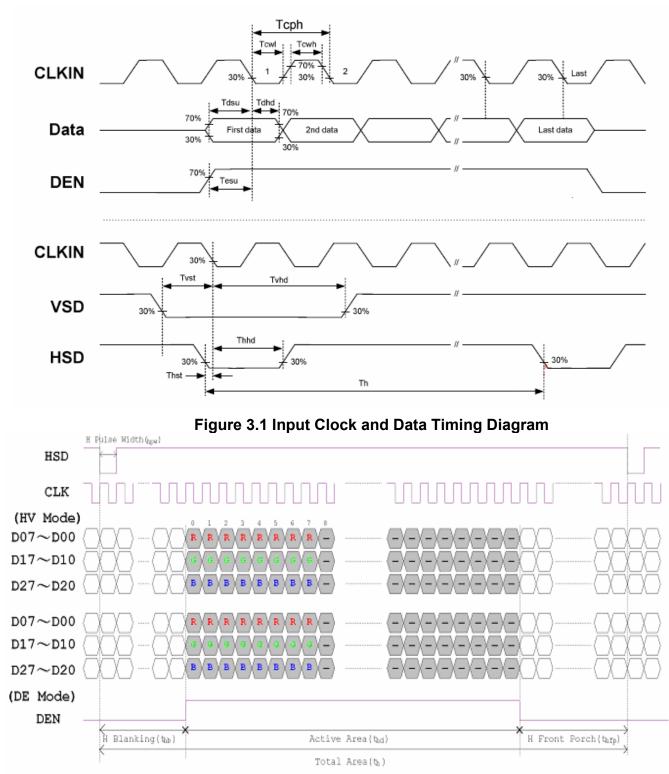


Figure 3.2 Horizontal input timing diagram.

3.3.3 Timing

Item	Symbol		Values	Unit	Remark	
nem	Symbol	Min.	Min. Typ.			
Horizontal Display Area	thd		800		DCLK	
DCLK Frequency	fclk		40	50	MHz	
One Horizontal Line	th	862	1056	1200	DCLK	
HS Pulse Width	thpw	1		40	DCLK	
HS Blanking	thb	46	46	46	DCLK	
HS Front Porch	thfp	16	210	354	DCLK	

ltem	Symbol		Values	Unit	Remark	
item	Symbol	Min.	Min. Typ.		Unit	Reindik
Vertical Display Area	tvd		600		TH	
VS Period Time	tv	624	635	700	TH	
VS Pulse Width	tvpw	1		20	TH	
VS Blanking	tvb	23	23	23	TH	
VS Front Porch	tvfp	1	12	77	TH	

4.0 OPTICAL SPECIFICATIONS

ltem	Symbol	Condition	Values			Unit	Remark	
item	Symbol	Condition	Min.	Тур.	Max.	Unit	Reillark	
	θL	Φ=180°(9 Ο'CLOCK)	60	70			Note 1	
Viewing Angle	θ_{R}	Φ=0°(3 O'CLOCK)	60	70		desuses		
(CR≥10)	θτ	Φ=90°(12 O'CLOCK)	60	70		degree		
	θ_{B}	Φ=270°(6 Ο'CLOCK)	40	50				
Response Time	T _{ON}			10	20	msec	Note 3	
Response rime	T _{OFF}			15	30	msec	Note 3	
Contrast Ratio	CR	Normal	400	500			Note 4	
Color Chromaticity	W _X	$\Theta = \Phi = 0^{\circ}$	0.26	0.31	0.36		Note 2 5 6	
	W _Y	0-Ψ-0	0.28	0.33	0.38		Note 2,5,6	
Luminance	L		300	350		cd/m ²	Note 6	
Luminance Uniformity YU			70	75		%	Note 7	

Test Conditions:

1. IL=200mA (Backlight current), the ambient temperature is 25° C.

2. The test systems refer to Note 2.

Note 1: Definition of viewing angle range

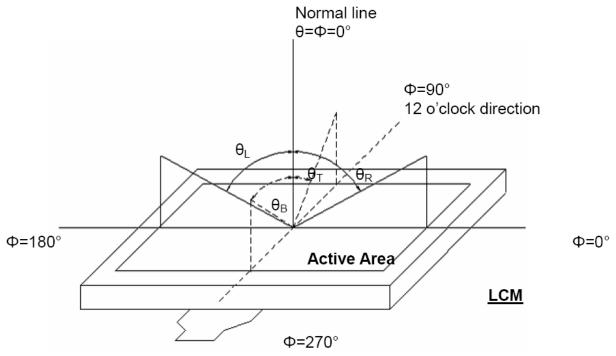


Figure 4.1 Definition of viewing angle.

Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON

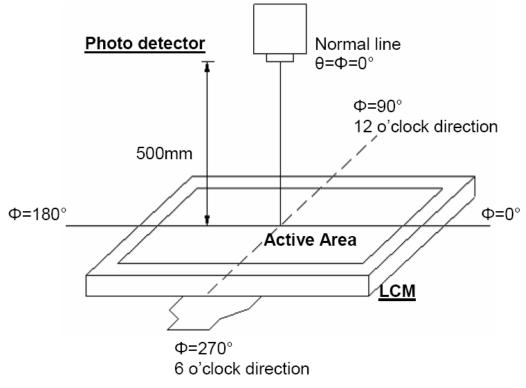


Figure 4.2 Optical measurement system setup

Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.

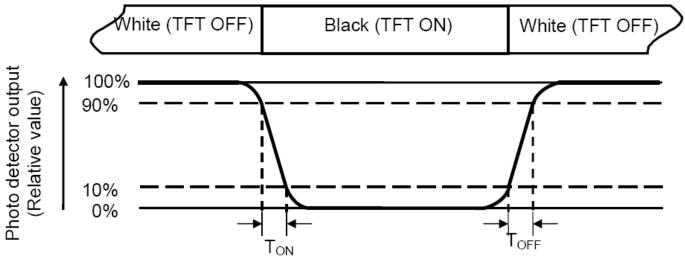


Figure 4.3 Definition of response.

Note 4: Definition of contrast ratio

 $Contrast ratio(CR) = \frac{Luminance measured when LCD on the "white" state}{Luminance measured when LCD on the "black" state}$

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel.

Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (Yu) = $\frac{B_{min}}{B_{max}}$

L-----Active area length

W----- Active area width

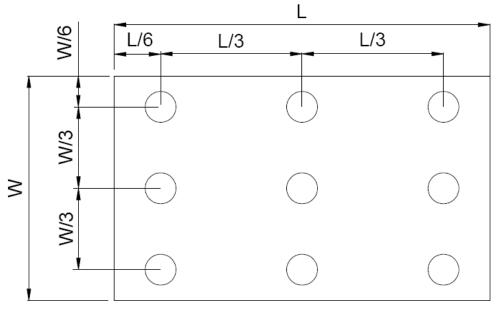


Figure 4.3 Definition of measuring points.

Bmax: The measured maximum luminance of all measurement position. Bmin: The measured minimum luminance of all measurement position.

5. RELIABILITY TEST

Item	Test Condition Item	Remark
High temperature storage	Ta= 80 °C 120hrs	Note 1 Note 4
Low temperature storage	Ta=-30 °C 120hrs	Note 1 Note 4
High temperature operation	Ts= 70 °C 120hrs	Note 2 Note 4
Low temperature operation	Ts=-20 °C 120hrs	Note 1 Note 4
High temperature/High humidity operation	90% RH 60°C 120hrs	Note 4
Thermal Shock	-30℃/30 min ~ +80℃/30 min for a total 50 cycles, Start with cold temperature and end with high temperature.	Note 4
Vibration test	Freq:10~55~10Hz Amplitude:1.5mm 1 hours for each direction of X,Y,Z (3 hours for total)	
Mechanical shock	50G 6ms,±X, ±Y, ±Z 3 times for each direction	
Package vibration test	Random Vibration : 0.015G*G/Hz from 5-200HZ, -6dB/Octave from 200-500HZ 1 hours for each direction of X. Y. Z. (3 hours for total)	
Package drop test	Height:60 cm 1 corner, 3 edges, 6 surfaces	
Electro static discharge	\pm 2KV, Human Body Mode, 100pF/1500Ω	

Note 1: Ta is the ambient temperature of samples.

Note 2: Ts is the temperature of panel's surface.

Note 3: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.

Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

6. PRECAUTION FOR USING LCM

- 1. When design the product with this LCD Module, make sure the viewing angle matches to its purpose of usage.
- 2. As LCD panel is made of glass substrate, dropping the LCD module or banging it against hard objects may cause cracking or fragmentation. Especially at corners and edges.
- 3. Although the polarizer of this LCD Module has the anti-glare coating, always be careful not to scratch its surface. Use of a plastic cover is recommended to protect the surface of polarizer.
- 4. If the LCD module is stored below specified temperature, the LC material may freeze and be deteriorated. If it is stored above specified temperature, the molecular orientation of the LC material may change to Liquid state and it may not revert to its original state. And also excessive temperature and humidity could cause polarizer peel off or bubble. Therefore, the LCD module should always be stored within specified temperature and humidity range. If the LCD modules will be stored for a long time, the recommend temperature/humidity for the storage environment is:

Temperature : 15° ~ 35° / Relatively humidity: $\leq 80\%$

Meanwhile please follow other requirements below for storage:
Store with no touch on display surface by the anything else. If possible, store the LCD in the packaging situation when it was delivered.

-If the original package is opened, please store in an anti-static polyethylene bag and seal it so as not to get fresh air outside enter into it.

- LCD modules shall be stored in a dark place. And it shall not be exposed to sunlight nor fluorescent light in storage.

Note: If the storage time is over 1 year, the golden fingers of FPC might be slightly oxidized, but it won't affect the electrical performance, customer can use rubber to clean the golden fingers before assembly or directly assemble the display.

6. Saliva or water droplets must be wiped off immediately as those may leave stains or cause color changes if is remained there for a long time. And water vapor will cause corrosion of ITO electrodes.

If the surface of LCD panel needs to be cleaned, wipe it swiftly with cotton or other soft dry cloth. If it is not still clean enough, blow a breath on the surface and wipe again.

If needed, please just moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 7. The module should be driven according to the specified ratings to avoid malfunction and permanent damage. Applying DC voltage cause a rapid deterioration of LC material. Make sure to apply alternating waveform by continuous application of the M signal. Especially the power ON/OFF sequence should be kept to avoid latch-up of driver LSIs and DC charge up to LCD panel.
- 8. Mechanical Considerations
 - a) LCM are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modifications. The following should be noted.
 - b) Do not tamper in any way with the tabs on the metal frame.
 - c) Do not modify the PCB by drilling extra holes, changing its outline, moving its components or modifying its pattern.
- 9. Static Electricity
 - a) Operator

Wear the electrostatics shielded clothes because human body may be statically charged if not ware shielded clothes. Never touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.

b) Equipment

There is a possibility that the static electricity is charged to the equipment, which has a function of peeling or friction action (ex: conveyer, soldering iron, working table). Earth the equipment through proper resistance (electrostatic earth: $1x10^8$ ohm).

Only properly grounded soldering irons should be used.

If an electric screwdriver is used, it should be well grounded and shielded from commutator sparks.

The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended.

c) Floor

Floor is the important part to drain static electricity, which is generated by operators or equipment.

There is a possibility that charged static electricity is not properly drained in case of insulating floor. Set the electrostatic earth (electrostatic earth: 1×10^8 ohm).

d) Humidity

Proper humidity helps in reducing the chance of generating electrostatic charges. Humidity should be kept between 50%RH and 80%RH.

e) Transportation/storage

The storage materials also need to be anti-static treated because there is a possibility that the human body or storage materials such as containers may be statically charged by friction or peeling.

The modules should be kept in antistatic bags or other containers resistant to static for storage.

f) Soldering

Soldering anything to this TFT display would void the warranty.

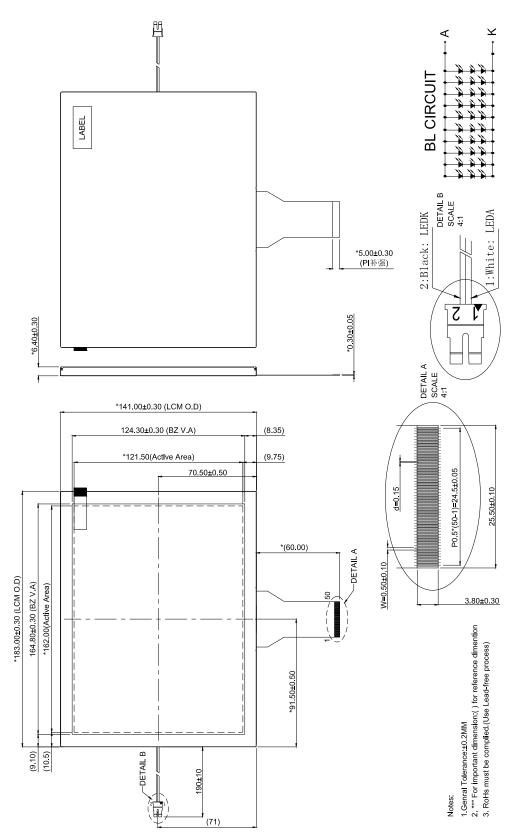
g) Others

The laminator (protective film) is attached on the surface of LCD panel to prevent it from scratches or stains. It should be peeled off slowly using static eliminator.

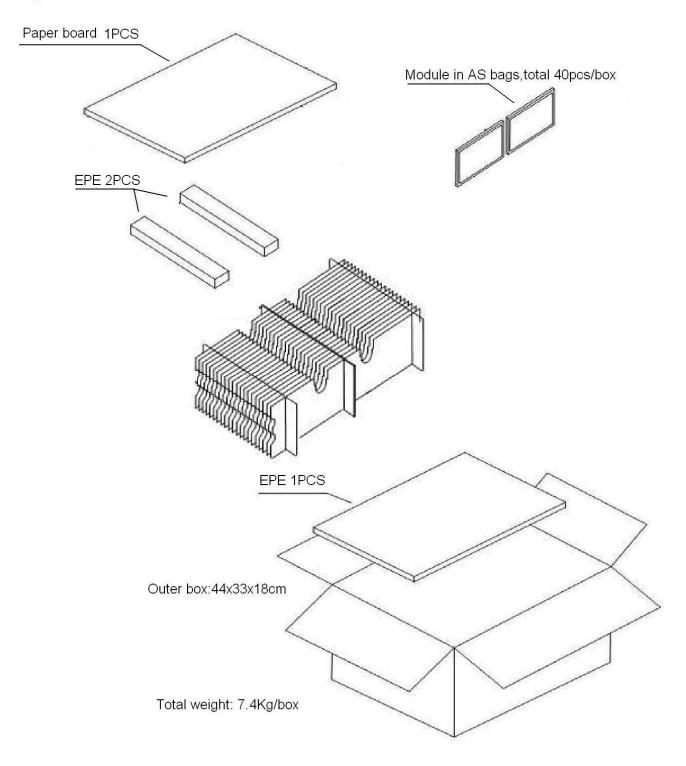
Static eliminator should also be installed to the workbench to prevent LCD module from static charge.

- 10. Operation
 - a) Driving voltage should be kept within specified range; excess voltage shortens display life.
 - b) Response time increases with decrease in temperature.
 - c) Display may turn black or dark blue at temperatures above its operational range; this is (however not pressing on the viewing area) may cause the segments to appear "fractured".
 - d) Mechanical disturbance during operation (such as pressing on the viewing area) may cause the segments to appear "fractured".
- 11. If any fluid leaks out of a damaged glass cell, wash off any human part that comes into contact with soap and water. The toxicity is extremely low but caution should be exercised at all the time.
- 12. Disassembling the LCD module can cause permanent damage and it should be strictly avoided.
- 13. LCD retains the display pattern when it is applied for long time (Image retention). To prevent image retention, do not apply the fixed pattern for a long time. Image retention is not a deterioration of LCD. It will be removed after display pattern is changed.
- 14. Do not use any materials, which emit gas from epoxy resin (hardener for amine) and silicone adhesive agent (dealcohol or deoxym) to prevent discoloration of polarizer due to gas.
- 15. Avoid the exposure of the module to the direct sunlight or strong ultraviolet light for a long time.

7. MECHANICAL DRAWING



8. PACKAGE DRAWING



9. INSPECTION SPECIFICATION

1. SCOPE SPECIFICATIONS CONTAIN

1.1 DISPLAY QUALITY EVALUATION

1.2 MECHANICS SPECIFICATION

2. SAMPLING PLAN

UNLESS THERE IS OTHER AGREEMENT, THE SAMPLING PLAN FOR INCOMING INSPECTION SHALL FOLLOW MIL-STD-105E.

- 2.1 LOT SIZE: QUANTITY PER SHIPMENT AS ONE LOT (DIFFERENT MODEL AS DIFFERENT LOT).
- 2.2 SAMPLING TYPE: NORMAL INSPECTION, SINGLE SAMPLING.
- 2.3 SAMPLING LEVEL: LEVEL II.
- 2.4 AQL: ACCEPTABLE QUALITY LEVEL MAJOR DEFECT: AQL=0.65 MINOR DEFECT: AQL=1.0

3. PANEL INSPECTION CONDITION

3.1 ENVIRONMENT:

ROOM TEMPERATURE: 25±5°C.

HUMIDITY: 65±5% RH.

ILLUMINATION: 300 ~ 700 LUX.

- 3.2 INSPECTION DISTANCE: 35±5 CM
- 3.3 INSPECTION ANGLE:
- THE VISION OF INSPECTOR SHOULD BE PERPENDICULAR TO THE SURFACE OF THE MODULE. 3.4 INSPECTION TIME:

PERCEPTIBILITY TEST TIME: 20 SECONDS MAX.

4. DISPLAY QUALITY

4.1 FUNCTION RELATED:

THE FUNCTION DEFECTS OF LINE DEFECT, ABNORMAL DISPLAY, AND NO DISPLAY ARE CONSIDERED MAJOR DEFECTS.

4.2 BRIGHT/DARK DOTS:

Defect Type	Specification	Major	Minor
Bright Dots	N≦3		•
Drak Dots	N≦4		•
Total Bright and Dark Dots	N≦6		•

Note: 1:

The definition of dot: The size of a defective dot over 1/2 of whole dot is regarded as one defective dot.

Bright dot: Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern. The bright dot defect must be visible through 2% ND filter

Dark dot: Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue pattern.

4.3 Pixel Definition:

R	G	В	R	G	В	R	G	В	Dot Defect
R	G	В	R	G	В	R	G	В	Adjacent Dot Defect
R	G		R	G		R	G	В	Cluster

Note 1:

If pixel or partial sub-pixel defects exceed 50% of the affected pixel or sub-pixel area, it shall be considered as1 defect.

Note 2:

There should be no distinct non-uniformity visible through 2% ND Filter within 2 sec inspection times.

4.4Visual Inspection specifications:

De	efect Type	Specification Size	Count (N)	Major	Minor
)ot shape	D≪0.25mm	Ignored		
	cratch and Bubbles in splay area)	0.25mm <d<0.5mm< td=""><td>N≪3</td><td></td><td>•</td></d<0.5mm<>	N≪3		•
\langle	- Q-	D>0.5mm	N=0		
		D≤70mm	N≪4		
Newton Ring	(Only for Touch panel)	D>70mm	N=0		•
TOD Fish Fyree		0.1mm <d≤0.2mm< td=""><td>N≪4</td><td></td><td></td></d≤0.2mm<>	N≪4		
	(Only for Touch panel) ubble/Dent)	0.2mm <d≪0.3mm< td=""><td>N≪3</td><td></td><td>•</td></d≪0.3mm<>	N≪3		•
(100		0.3mm <d≤0.4mm< td=""><td>N≤2</td><td></td><td></td></d≤0.4mm<>	N≤2		
	ine shape	W≪0.01mm	Ignored		
	atch、Lint and Bubbles isplay area)	0.01mm $<$ W \leqslant 0.05mm, and L \leqslant 3mm	N≪3		•
		W $>$ 0.05mm,or L $>$ 3mm	N=0		
Bubble in	cell (active area)	It should be found by eyes		•	
	Scratch	No harm		•	
Bezel	Dirt	No harm			•
Dezei	Wrap	No harm		•	
	Sunken			•	
	No label	No		•	
	Inverted label	No			•
	Broken	No		•	
Label	Dirt	Word can be read		•	
Laber	Not clear	No		•	
	Word out of shape	No			•
	Mistake	No		•	
	Position	Be attached on right position	ached on right position		
Screw	Not enough	No		•	
Screw	Limp	No			•
Connector	Connection status	No bend on PINs and damage		•	
FPC/FFC	Broken	No		•	

Note: Extraneous substance and scratch not affecting the display of image, for instance, extraneous substance under polarizer film but outside the display area, or scratch on metal bezel and backlight module or polarizer film outside the display area, shall not be considered as defective or non-conforming.